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Rob V. van Nieuwpoort, Jason Maassen, Rutger Hofman, Thilo Kielmann, Henri E. Bal  
 November 2002 **JGI '02: Proceedings of the 2002 joint ACM-ISCOPE conference on Java**  
**Publisher:** ACM

Full text available: Pdf (120.05 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [cited review](#)
**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 40, Citation Count: 4

In computational grids, performance-hungry applications need to simultaneously tap computational power of multiple, dynamically available sites. The crux of designing grid environments stems exactly from the dynamic availability of compute ...

**Keywords:** Java, grid computing, performance, portability
2 **Compact Java binaries for embedded systems**

Derek Rayside, Evan Mamas, Erik Hons  
 November 1999 **CASCON '99: Proceedings of the 1999 conference of the Centre for Advanced Collaborative research**

**Publisher:** IBM Press

Full text available: Pdf (124.35 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [cited review](#)
**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 21, Citation Count: 11

Embedded systems bring special purpose computing power to consumer electronics such as smartcards, CD players and pagers. Java is being aggressively targeted at such system initiatives such as the Java 2 Platform, Micro Edition, which introduces ...

3 **\*J: a tool for dynamic analysis of Java programs**

Bruno Dufour, Laurie Hendren, Clark Verbrugge  
 October 2003 **OOPSLA '03: Companion of the 18th annual ACM SIGPLAN conference on programming, systems, languages, and applications**

**Publisher:** ACM [Request Permissions](#)

Full text available: Pdf (58.99 KB)

 Additional Information: [full citation](#), [abstract](#), [references](#), [cited review](#)
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We describe a complete system for gathering, computing and presenting dynamic metrics for programs. The system itself was motivated from our real goals in understanding program behavior as compiler/runtime developers, and so solves a number of ...


**Keywords:** Java, dynamic metrics, execution traces, profiling, program analysis, software
4 **ObjectGlobe: Ubiquitous query processing on the Internet**

R. Braumandl, M. Keidi, A. Kemper, D. Kossmann, A. Kreutz, S. Seltzsch, K. Stocker

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**Publisher:** Springer-Verlag New York, Inc.Full text available:  Pdf (251.44 KB)**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited](#)**Bibliometrics:** Downloads (6 Weeks): 6, Downloads (12 Months): 74, Citation Count: 19

We present the design of ObjectGlobe, a distributed and open query processor for InI sources. Today, data is published on the Internet via Web servers which have, if at a query processing capabilities. The goal of the ObjectGlobe ...

**Keywords:** Cycle-, function- and data provider, Distributed query processing, Open Quality of service, Query optimization, Security



## 5 [An innovative design approach to build virtual environment systems](#)

 M. Oliveira, J. Crowcroft, M. SlaterMay 2003 **EGVE '03:** Proceedings of the workshop on Virtual environments 2003**Publisher:** ACMFull text available:  Pdf (781.01 KB)**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited](#)**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 43, Citation Count: 3

A Virtual Environment (VE) presents a complex problem with interesting non-trivial c software development. The majority of existing systems supporting VE are based on architectures, making maintenance and software reuse difficult ...

**Keywords:** component frameworks, java, online games, system design, virtual envi reality

## 6 [Runtime safety analysis of multithreaded programs](#)

 Koushik Sen, Grigore Rosu, Gul AghaSeptember 2003 **ESEC/ FSE-11:** Proceedings of the 9th European software engineering c jointly with 11th ACM SIGSOFT international symposium on Foundations engineering**Publisher:** ACM Full text available:  Pdf (583.43 KB)**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited](#)**Bibliometrics:** Downloads (6 Weeks): 9, Downloads (12 Months): 74, Citation Count: 13


Foundational and scalable techniques for runtime safety analysis of multithreaded prc explored in this paper. A technique based on vector clocks to extract the causal depe state updates from a running multithreaded program is ...

**Keywords:** JMpAX, Java, LTL, multithreaded program, predictive analysis, runtime n analysis, vector clock

Also published in:

September 2003 **SIGSOFT Software Engineering Notes** Volume 28 Issue 5

## 7 [MJ: a rational module system for Java and its applications](#)

 John Corwin, David F. Bacon, David Grove, Chet MurthyNovember 2003 **OOPSLA '03:** Proceedings of the 18th annual ACM SIGPLAN conference oriented programming, systems, languages, and applications**Publisher:** ACM Full text available:  Pdf (208.83 KB)**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited](#)**Bibliometrics:** Downloads (6 Weeks): 5, Downloads (12 Months): 32, Citation Count: 6


While Java provides many software engineering benefits, it lacks a coherent module : instead provides only packages (which are primarily a name space mechanism) and c (which are very low-level). As a result, large Java applications ...

**Keywords:** Java, components, language design, modularity

Also published in:


November 2003 **SIGPLAN Notices** Volume 38 Issue 11

8 [Proof linking: modular verification of mobile programs in the presence of lazy, dy](#)

 Philip W. L. Fong, Robert D. Cameron

October 2000 **Transactions on Software Engineering and Methodology (TOSEM)** .

**Publisher:** ACM  [Request Permissions](#)


Full text available:  Pdf (233.60 KB) **Additional Information:** [full citation](#), [abstract](#), [references](#), [cited review](#)

**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 32, Citation Count: 1

Although mobile code systems typically employ link-time code verifiers to protect hos from potentially malicious code, implementation flaws in the verifiers may still leave t vulnerable to attack. Compounding the inherent complexity ...

**Keywords:** Java, correctness conditions, dynamic linking, mobile code, modularity, ; safety, verification protocol, virtual machine architecture

9 [Reducing stack usage in Java bytecode execution](#)

 Wes Munsil, Chia-Jiu Wang

March 1998 **SIGARCH Computer Architecture News** . Volume 26 Issue 1

**Publisher:** ACM

Full text available:  Pdf (1.04 MB) **Additional Information:** [full citation](#), [abstract](#), [references](#)

**Bibliometrics:** Downloads (6 Weeks): 3, Downloads (12 Months): 9, Citation Count: 0

For many years, the Tomasulo method of dynamically scheduling instructions for exe load/store processor has been known and used. This paper presents an adaptation of method to a stack-based processor architecture, and illustrates ...

10 [A java virtual machine architecture for very small devices](#)

 Nik Shaylor, Douglas N. Simon, William F. Bush

July 2003 **LCTES '03: Proceedings of the 2003 ACM SIGPLAN conference on Language tool for embedded systems**

**Publisher:** ACM  [Request Permissions](#)

Full text available:  Pdf (182.85 KB) **Additional Information:** [full citation](#), [abstract](#), [references](#), [cited](#)

**Bibliometrics:** Downloads (6 Weeks): 10, Downloads (12 Months): 103, Citation Count: 3

The smallest complete Java™ virtual machine implementations in use today are base standard and are deployed in mobile phones and PDAs. These implementations requi kilobytes. Smaller Java-like implementations also ...

**Keywords:** CLDC, JVM, java, limited-memory devices, next generation smart cards

Also published in:

July 2003 **SIGPLAN Notices** Volume 38 Issue 7



## Proxy compilation of dynamically loaded Java classes with Mojo

Matt Newsome, Des Watson

July 2002 **LCTES/ SCOPES '02**: Proceedings of the joint conference on Languages, cor  
for embedded systems: software and compilers for embedded systems

**Publisher:** ACM [Request Permissions](#)

Full text available: [Pdf \(358.62 KB\)](#)

**Additional Information:** [full citation](#), [abstract](#), [references](#), [index](#)

**Bibliometrics:** Downloads (6 Weeks): 4, Downloads (12 Months): 29, Citation Count: 1

Interest in Java implementations for resource-constrained environments such as emt  
has been tempered by concerns regarding its efficiency. Current native compilers for  
dramatic increases in efficiency, but have poor support for ...

**Keywords:** AOT, JIT, adaptive compilation, ahead-of-time, dynamic class loading, h  
time, native compilation, proxy compilation, remote compilation

Also published in:

July 2002 **SIGPLAN Notices** Volume 37 Issue 7

## 12 Performance monitoring of java applications



M. Harkema, D. Quartel, B. M. M. Gijzen, R. D. van der Mei

July 2002 **WOSP '02**: Proceedings of the 3rd international workshop on Software and J

**Publisher:** ACM [Request Permissions](#)

Full text available: [Pdf \(219.69 KB\)](#)

**Additional Information:** [full citation](#), [abstract](#), [references](#), [cited](#)

**Bibliometrics:** Downloads (6 Weeks): 11, Downloads (12 Months): 113, Citation Count: 3

Over the past few years, Java has evolved into a mature platform for developing ente  
applications. A critical factor for the commercial success of these applications is end-1  
performance, e.g., in terms of response times, throughput and availability. ...

**Keywords:** performance measurement and monitoring of java applications

## 13 Java as a specification language for hardware-software systems

Rachid Helaihel, Kunle Olukotun

November 1997 **ICCAD '97**: Proceedings of the 1997 IEEE/ACM international conference  
aided design

**Publisher:** IEEE Computer Society

Full text available: [Publisher Site](#), [Pdf \(46.54 KB\)](#) **Additional Information:** [full citation](#), [abstract](#), [refers](#)  
[terms](#)

**Bibliometrics:** Downloads (6 Weeks): 1, Downloads (12 Months): 14, Citation Count: 29

The specification language is a critical component of the hardware-software co-design  
is used for functional validation and as a starting point for hardware- software partiti  
synthesis. This paper pro poses the Java programming ...

**Keywords:** java, specification languages, hardware-software co-design

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